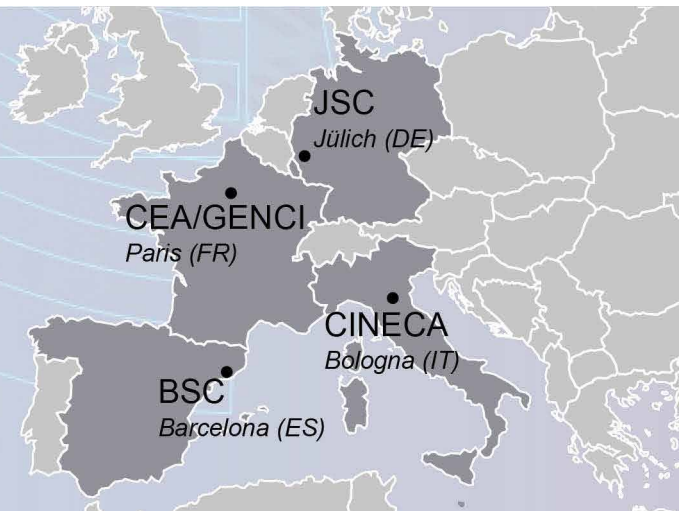


For the first time in Europe, academia and industry join forces in a public procurement process to build the HPC infrastructure of the future



A group of leading European supercomputing centres formed in 2017 a buyers group to execute a **joint Public Procurement of Innovative Solutions (PPI)** for the first time in the area of high-performance computing (HPC). The co-funding by the European Commission allows for a significant enhancement of the planned supercomputing infrastructure from 2019 and **paved the way for future joint investments in Europe, e.g. in the context of EuroHPC**. The total investment is planned to be about € 73 million. The participating HPC centres, namely BSC (Spain), CEA/GENCI (France), CINECA (Italy) and Forschungszentrum Jülich (Germany), have a strong track record in providing supercomputing resources at the European level.

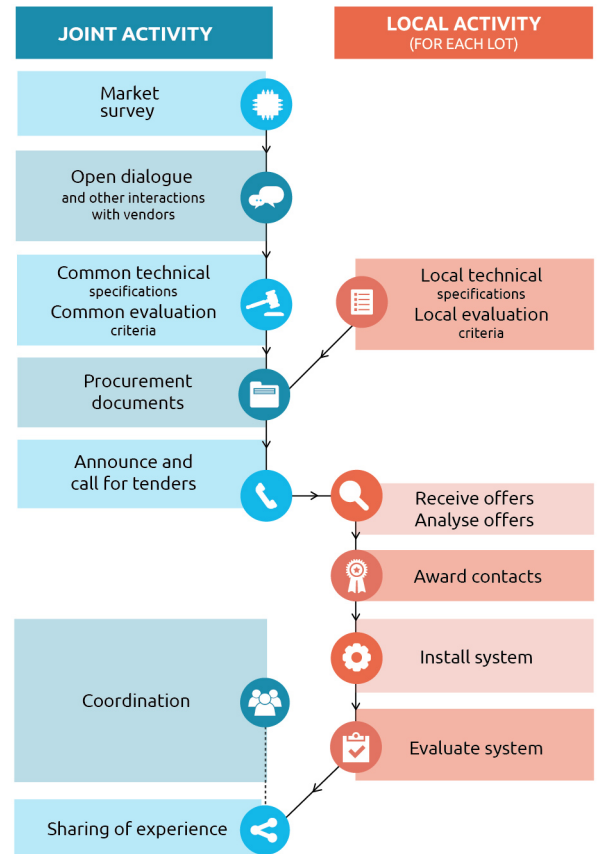
The objective of this PPI is to buy innovative, high-performance supercomputers and/or innovative high-performance storage systems.

This joint initiative will create multiple benefits:

- **More innovative supercomputing resources will be efficiently exploitable** for science and engineering applications in Europe within PRACE, the pan-European HPC infrastructure, as selected, relevant applications will play an important role in guiding this procurement process.
- **R&D on HPC architectures and technologies in Europe will be strengthened**
- **The coordinated approach will give buyers a greater weight** and allow for greater impact on the design of the solutions according to the need of scientists and engineers in Europe.

Four systems will be deployed in the period 2019-2021. In combination, these systems will power a wide range of applications, including traditional HPC applications, HPDA and AI.

AT A GLANCE	
Programme:	Horizon 2020
Duration:	01.04.2017 - 30.09.2021
Main Outcome:	Selection and buying of innovative, high-performance supercomputers and/or innovative high-performance storage systems.



Joint procurement process as implemented by PPI4HPC

